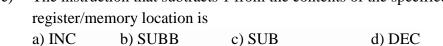
Enrollme	ent No: Exam Seat No:	
	C.U.SHAH UNIVERSITY	
	Winter Examination-2015	
Subject (Semester Instruction		
(3) D	nstructions written on main answer book are strictly to be obeyed. Draw neat diagrams and figures (if necessary) at right places. Assume suitable data if needed.	_
	Attempt the following questions:	
a)	If the data is present in a register and it is referred using the particular register, then it is a) direct addressing mode b) register addressing mode	1
	c) indexed addressing mode d) immediate addressing mode	
b)	The assembler directives which are the hints using some predefined alphabetical strings are given to a) processor b) memory c) assembler d) processor & assembler	1
c)	As the storing of data words onto the stack is increased, the stack pointer is a) incremented by 1 b) decremented by 1 c) incremented by 2 d) decremented by 2	1
d)	The time required for execution of a macro is that of procedure. a) greater than b) less than c) equal to d) none of the mentioned	1
e)	The instruction that subtracts 1 from the contents of the specified register/memory location is	1



f)

Q-1

program and partial results in the secondary storage called a) mapping b) swapping in c) swapping out d) pipelining One of the main feature that distinguish microprocessors from micro-computers 1 g)

1

The process of making the physical memory free by storing the portion of

is a. Words are usually larger in microprocessors

b. Words are shorter in microprocessors



		d. Exactly the same as the machine cycle time	
	h)	The Intel 8086 microprocessor is a processor . a. 8 bit b. 16 bit c. 32 bit d. 4 bit	1
	i)	The 16 bit flag of 8086 microprocessor is responsible to indicate a. the condition of result of ALU operation b. the condition of memory c. the result of addition d. the result of subtraction	1
	j)	Which microprocessor has multiplexed data and address lines? a. 8086 b. 80286 c. 80386 d. Pentium	1
	k)	20-bit address bus can locate a. 1,048,576 locations	1
	1)	The directive used to inform the assembler, the names of the logicals segments to be assumed for different segments used in the program is a) ASSUME b) SEGMENT c) SHORT d) DB	1
	m)	Which method bypasses the CPU for certain types of data transfer? a. Software interrupts b. Interrupt-driven I/O c. Polled I/O d. Direct memory access (DMA)	1
	n)	The register AX is formed by grouping a. AH & AL b. BH & BL c. CH & CL d. DH & DL	1
	Attem	pt any four questions from Q-2 to Q-8	
Q-2	A B	Attempt all questions Explain the internal architecture of 8085 microprocessor with diagram. What is bus? List and Explain different types of bus with diagram.	(07) (07)
Q-3	A B	Attempt all questions What is memory bank? Explain 8086 memory bank with diagram. Explain working of 8086 in minimum mode with diagram.	(07) (07)
Q-4	A B	Attempt all questions Write an assembly language program in 8086 to subtract two 32 bit numbers. Write a assembly language program in 8086 to move string of data bytes from one memory location to another memory location using string instruction.	(07) (07)

c. Microprocessor does not contain I/O devices



		Attempt all questions	
Q-5	A	What is addressing mode? Explain any 6 addressing modes of 8086 with example.	(07)
	В	What is memory segmentation? Explain segmented memory organization of 8086 microprocessor with necessary diagram.	(07)
		Attempt all questions	
Q-6	A	List 80386 descriptor table registers? Explain any 2 descriptor table registers.	(07)
	В	With a diagram explain internal architecture of 80286 microprocessor.	(07)
		Attempt all questions	
Q-7	A	What is descriptor? Explain LDT and GDT.	(07)
	В	Explain with diagram how 80386 converts logical address to physical address using paging.	(07)
		Attempt all questions	
Q-8	A	List and explain any 5 features of Pentium processor.	(05)
	В	Compare RISC and CISC.	(05)
	C	List and explain any 4 features of Intel core2 duo desktop processor.	(04)

